

## AMENDMENTS

Please amend the application as follows:

### In the Claims

Please substitute the following clean copy text for the pending claims of the same number.

- Sub B1*
1. (amended once) A transistor circuit for implementing a switch, comprising:
- a first switch node configured to connect to an external circuit;
  - a second switch node configured to connect to the external circuit;
  - a transistor device having a first terminal electrically communicating with the first switch node, a second terminal connected to the second switch node, and a third terminal configured to receive a control signal that controls the electrical connectivity between the first terminal and the second terminal;
  - a third switch node for receiving the control signal; and
  - a circuit connected to the third switch node and the third terminal of the transistor device, the circuit having a sufficiently high impedance to prevent the third switch node from functioning as an alternating current (AC) ground during operation of the switch.

- SUB C3*
6. (amended once) A transistor circuit for implementing a differential switch, comprising:
- a first switch node configured to connect to an external circuit;
  - a second switch node configured to connect to the external circuit;